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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
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EXAMINER

PATEL, NITIN C

ART UNIT	PAPER NUMBER
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2116

DATE MAILED: 01/09/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

10/091,031

Applicant(s)

CIOACA, DUMITRU

Examiner

Nitin C. Patel

Art Unit

2116

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 23 November 2005.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-43 is/are pending in the application.
- 4a) Of the above claim(s) 2, 14, 16, 18-23, 25, 27-35, 38, 40 and 42 is/are withdrawn from consideration.
- 5) ☒ Claim(s) 1, 3-13, 36-37, and 41 is/are allowed.
- 6) ☒ Claim(s) 15, 24, 26 and 39 is/are rejected.
- 7) ☒ Claim(s) 17 and 43 is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

1. This is in responsive to request for continued examination filed on 23 November 2005.
2. Claims 2, 14, 16, 18 – 23, 25, 27 – 35, 38, 40, and 42 have been cancelled.

Claim Objections

3. Claim 43 is objected to because of the following informalities:
 4. In the claim 43, delete ---said output--- following the words "to said output," in line 1, on page 8, since those words are duplicated.
- Appropriate correction is required.

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

5. Claim 15, is rejected under 35 U.S.C. 102(b) as being clearly anticipated by Frank et al. [hereinafter as Frank], US Patent 6,348, 886 B1.
6. As to claim 15, Frank discloses a method of controlling power supply comprising:
 - a. producing an analog signal [Vout] related to a number of bits having a particular logic state [detected by control logic] in a digital signal, said analog signal being independent of an output of said power supply [col. 1, lines 17 – 20, 66 – 67, col. 2, lines 1 – 8]; and

b. setting an output of said power supply [106, reference voltage generator] to a particular level in response to said analog signal [Vout], wherein said power supply [106] comprises a plurality of sub circuits [126 charge pump, 128 charge capacitor, 130 switch] and said setting an output of a power supply comprises activating [enabling] more than one of said sub circuits [126, 128][col. 3, lines 6 – 29, col. 4, lines 30 – 50].

7. Claims 24, 26, and 39 are rejected under 35 U.S.C. 102(b) as being clearly anticipated by Yousefi et al. [hereinafter as Yousefi], US Patent 5,448, 598.

8. As to claim 24, Yousefi discloses a method of controlling a power supply comprising:

a. sensing a number of bits in a particular logic state [sensing data pattern] in a particular digital communication [col. 7, lines 56 – 58] ; and

b. adapting said power supply to supply a particular level of current, said level of current being proportional to said number of bits by switchingly connecting a plurality of power supply portions to a load circuit [col. 6, lines 50 – 67, col. 7, lines 1 – 30, and 56 – 66].

9. As to claim 26, Yousefi teaches plurality of power supply portions including a charge pump circuit [22, variable current charge pump, fig. 4][col. 6, lines 50 – 67 col. 7, lines 24 – 30].

10. As to claim 39, Yousefi discloses a power supply controller comprising:

a. a plurality of data bus inputs [RAW DATA, fig. 4];

b. a plurality of outputs;

c. a sensing circuit [inherent to data pattern sensing] adapted to activate one or more of said plurality of outputs [lout] in response to a corresponding pattern of data bus signals detected on said plurality of data bus inputs and a plurality of charge pump circuits [22 charge pump] connected to one of said plurality of outputs [col. 7, lines 52 – 66] .

11. Claims 1, 3 – 13, 36 – 37, and 41, are allowed and claim 43 will be allowed after correction to the objection.

12. **Examiner's note:** Examiner has cited particular columns and line numbers in the references as applied to the claims above for the convenience of the applicant.

Although the specified citations are representative of the teachings of the art and are applied to the specific limitations within the individual claim, other passages and figures may apply as well. It is respectfully requested from the applicant in preparing responses, to fully consider the references in entirety as potentially teaching all or part of the claimed invention, as well as the context of the passage as taught by the prior art or disclosed by the Examiner.

13. **Prior Art not relied upon:** Please refer to the references listed in attached PTO-892, which, are not relied upon for claim rejection since these references are relevant to the claimed invention.

Allowable Subject Matter

14. Claim 17 is objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

Reasons For Allowance

15. The following is an examiner's statement of reasons for allowance: Applicant's claimed invention distinguishes over the prior art for following reasons.

The independent claims 1,36, and 37, are allowable in view of applicant's arguments regarding the information relating to an anticipated change in demand over the art of record and none of the references either alone or in combination, discloses or renders obvious a power supply apparatus and method for controlling a power supply to control power including activating more than one of plurality of charge pump circuits responsive to control signal generated based on signal information relating to an anticipated change in power demand as claimed in invention.

The independent claims 41, and 43, are allowable in view of applicant's arguments regarding the sensing circuit adapted to activate more than one of the plurality of outputs in response to a pattern of data bus signals over the art of record and none of the references either alone or in combination, discloses or renders obvious a processing system having a memory device including a power controller having a plurality of outputs, a plurality of charge pump circuits each operatively connected to respective output, and a sensing circuit adapted to activate more than one of the plurality of outputs in response to data pattern as claimed in invention.

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Nitin C. Patel whose telephone number is 571-272-3675. The examiner can normally be reached on 6:45 am - 5:15 pm.

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Lynne H. Browne can be reached on 571-272-3670. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Nitin C. Patel
January 4, 2006


LYNNE H. BROWNE
SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2100